

AMENDMENTS TO THE SPECIFICATION

Please ADD the following heading and paragraph on page 1 after the Title of the Invention as follows:

CROSS-REFERENCE TO RELATED APPLICATIONS

This application is a U.S. National Stage entry of co-pending International Patent Application No. PCT/EP02/14889, filed on December 27, 2002 by Di Maio, Luciano et al. entitled HIGH SPEED TRANSFER SYSTEM, the entire contents of which is incorporated by reference, and for which priority is claimed under 35 U.S.C. § 371. As in the parent International Application No. PCT/EP02/14889, priority is also claimed to co-pending European Patent Application No. 01205188.4, filed on December 31, 2001, the entire contents of which is incorporated by reference for which priority is claimed under 35 U.S.C. § 119.

Please AMEND the heading at page 1, line 2 to read as follows:

DESCRIPTION BACKGROUND OF THE INVENTION

Please REPLACE the paragraph at page 1, line 22 with the following replacement paragraph:

In same-some cases it is possible to implement on such device even the protection functions. This is possible because the hardware allows it to do that.

Please ADD the following heading at page 2, before line 14 as follows:

SUMMARY OF THE INVENTION

Please ADD the following headings and paragraph at page 2, before line 25 as follows:

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 illustrates an embodiment of a high-speed transfer system utilizing a logic function;

FIG. 2 illustrates the high-speed transfer system utilizing a different logic function than in FIG. 1;

FIG. 3 illustrates conventional architecture of a high-speed transfer system; and

FIG. 4 illustrates a software architecture useful in an aspect of an embodiment of a high-speed transfer system.

DETAILED DESCRIPTION